



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2822
Examiner: Not yet assigned
Confirmation No.: 2321

In re PATENT APPLICATION of:

Applicant(s): Motoki KOBAYASHI et al.

Serial No.: 10/673,499

Filed: September 30, 2003

For: SEMICONDUCTOR DEVICE AND
SEMICONDUCTOR DEVICE
FABRICATION METHOD

Docket No.: FUJI 137

**SUBMISSION OF
FORMAL DRAWINGS**

April 1, 2004

MAIL STOP PGPUB

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants hereby submit seven (7) sheets of corrected Formal Drawings (Figs. 1-10).

Respectfully submitted,

April 1, 2004
Date

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RHB:crh

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